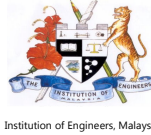


TECHNICAL TALK AND PROFESSIONAL NETWORKING SESSION

Jointly Organised By



Recent trends in the field of Design for Testability

By

Professor Andrew Richardson
Lancaster University

BEM Approved
CPD Hours: 2.5
IEM10/PG/332/T

26th November 2010 (Friday)

2.00pm - 5.00pm

USAINS Auditorium 1, Kompleks Eureka, USM Main Campus, Penang

Abstract:

Design for Testability has been an important discipline within the Electronic System Design community for over 30 years but recently the field has expanded due to complexity, variability in sub 60nm technologies and increased integration of non-digital functionality. This talk will brief the audience on the work active at Lancaster University in the area of test engineering and the current international trends and "hot topics" in Design for Testability recently debated at the International Test Conference in Austin Texas and the European Test Symposium. In particular the talk will cover:

- Recent work in Analogue and Mixed Signal Design for Testability
- Test issues and solutions for MEMS integration
- Handling Variability and adaptive test
- Emerging standards to complement 1149.1

Biography:

Professor Andrew Richardson holds a personal chair at Lancaster University in Microsystems Engineering. He is a specialist in reliability & test with a background in the field of mixed signal and analogue electronics. Over the past 10 years Andrew has delivered on European contracts involving Design for Testability work on high resolution converters with AMI Semiconductors and Dolphin Integration, on Analogue front ends and PLL's with Philips and NXP and in MEMS testing with ST Microelectronics and QinetiQ. Over the past 5 years Andrew has led a European Network of Excellence in Design for Micro & Nano Manufacture that aimed to bring the reliability and test communities together. This activity involved 150 Engineers from 24 pan-European partners. He is also an evaluator and reviewer for the EC ICT and NMP programs, a steering committee member for the IEEE workshop IMS3TW and a program committee member for the European Test Symposium. Andrew also now runs EnablingMNT, UK that is part of the EnablingMNT group with partner offices located in Holland and Germany.

FREE ADMISSION!

Agenda:

2.00-2.30pm	Registration & Networking
2.30-4.00pm	Technical Talk
4.00-4.30pm	Q & A Session
4.30-5.00pm	Refreshment & Networking

Register online now at

<http://dreamcatcher.asia/TechTalk.html>

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